

T1210T-6I, T1220T-6I T1225T-6I, T1235T-6I

Entry level Snubberless™, logic level and standard 12 A TRIACs

Features

- Medium current TRIAC
- High static and dynamic commutation
- Low thermal resistance with clip bonding
- Packages is RoHS (2002/95/EC) compliant
- V_{RM} = 600 V
- Insulated packaged rated at 2500 V rms

Applications

- Entry-level and value-sensitive applications
- General purpose AC line load switching
- Motor control circuits in power tools
- Small home appliances, lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

Description

Available in through-hole, the T12T series of TRIACs can be used as on/off or phase angle control function in general purpose AC switching where high commutation capability is required.

Initially planned for power tools (T series) equipment, this series can be used in many value sensitive appliances thanks to the guidance on parameters provided in the following pages.

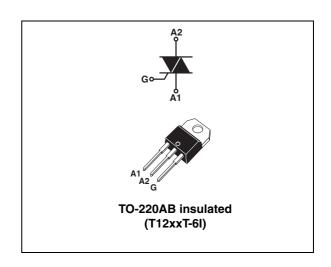


Table 1. Device summary

Order code	Symbol	Value			
T1220T-6I T1235T-6I	I _{GT} 3Q Snubberless	20 / 35 mA			
T1225T-6I	I _{GT} 4Q standard	25 mA			
T1210T-6I	I _{GT} 3Q logic level	10 mA			

TM: Snubberless is a trademark of STMicroelectronics

1 Characteristics

Table 2. Absolute maximum ratings (limiting values; $T_j = 25$ °C, unless otherwise specified)

Symbol	Parameter				Unit	
I _{T(RMS)}	On-state rms current (full sine wave) $T_c = 88^{\circ}$		T _c = 88 °C	12	Α	
1.	Non repetitive surge peak on-state current (full	ate current (full F = 50 Hz		90	Α	
ITSM	cycle, T _j initial = 25 °C)	F = 60 Hz	$t_p = 16.7 \text{ ms}$	95	A	
l ² t	I^2t Value for fusing $t_p = 10 \text{ ms}$			60	A ² s	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ $t_r \le 100 \text{ ns}$	F = 60 Hz	T _j = 125 °C	50	A/µs	
V _{DSM} / V _{RSM}	Non repetitive surge peak off-state voltage $t_p = 10 \; \text{ms}$		T _j = 25 °C	V _{DRM} /V _{RRM} + 100	V	
I _{GM}	Peak gate current $t_p = 20 \ \mu s$ $T_j = 125$		T _j = 125 °C	4	Α	
P _{G(AV)}	Average gate power dissipation $T_j = 125 ^{\circ}\text{C}$				W	
T _{stg}	Storage junction temperature range			- 40 to + 150	°C	
T _j	Operating junction temperature range			- 40 to + 125	°C	

Table 3. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Compleal	Test conditions	Oundrant		T12xxT			11	
Symbol	Test conditions	Quadrant		T1210T	T1220T	T1225T	T1235T	Unit
ı (1)	$V_D = 12 \text{ V} R_L = 30 \Omega$	1 - 11 - 111	MAX.	10	20	25	35	mA
I _{GT} ⁽¹⁾		IV				40		
V _{GT}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega, \ T_j = 25 ^{\circ}\text{C}$ ALL MAX. 1.3		1.3			V		
V _{GD}	$V_D = V_{DRM}$, $R_L = 3.3 \text{ k}\Omega$, $T_j = 125 ^{\circ}\text{C}$	ALL	MIN.	0.2		V		
I _H ⁽²⁾	I _T = 500 mA		MAX.	10	15	20	30	mA
		I - III		20	35	40	50	
IL	I _G = 1.2 I _{GT}	IV	MAX.			40		mA
		II		30	40	60	80	
dV/d+ (2)	V _D = 67% V _{DRM} , gate open	T _j = 125 °C	MIN.	100	1000	100	2000	V/µs
u v/ui · /	$v_D = 67\% v_{DRM}$, gate open	$T_j = 150 {}^{\circ}C^{(3)}$	IVIIIN.	50	500	50	1000	V/μS
	$(dV/dt)c = 0.1 V/\mu s$			7		7		
	(dV/dt)c = 10 V/μs	T _j = 125 °C		3		3		
(di/dt)c (2)	Without snubber	NAIN .	MIN.		6		12	A/ms
(ul/ul)c (=)	$(dV/dt)c = 0.1 V/\mu s$		IVIIIN.	3		3		Ailis
	(dV/dt)c = 10 V/µs	$T_j = 150 {}^{\circ}C^{(3)}$		1		1		
	Without snubber				3		10	

- 1. Minimum $I_{\mbox{\footnotesize{GT}}}$ is guaranted at 5% of $I_{\mbox{\footnotesize{GT}}}$ max.
- 2. For both polarities of A2 referenced to A1.
- 3. Derating information for excess temperature above T_j max.

Table 4. Static characteristics

Symbol	Test conditions		Value		Unit
V _T ⁽¹⁾	$I_{TM} = 17 \text{ A}, t_p = 380 \ \mu \text{s}$	T _j = 25 °C	Max.	1.55	V
V _{TO} (1)	Threshold voltage	T _j = 125 °C	Max.	0.85	V
R _D ⁽¹⁾	Dynamic resistance	T _j = 125 °C	Max.	35	mΩ
_	$V_{DRM} = V_{RRM}$	T _j = 25 °C	Max.	5	μΑ
I _{DRM}		T _j = 125 °C	IVIAX.	1	A
IRRM	$V_D = 0.9 \times V_{DRM}$	$T_j = 150 {}^{\circ}C^{(2)}$	Тур. 1.9		- mA

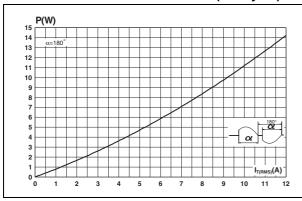
- 1. For both polarities of A2 referenced to A1.
- 2. Derating information for excess temperature above T_{i} max.

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Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	2.6	°C/W
R _{th(j-a)}	Junction to ambient (DC)	60	°C/W

Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case rms on-state current (full cycle) temperature (full cycle)



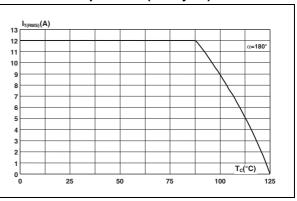
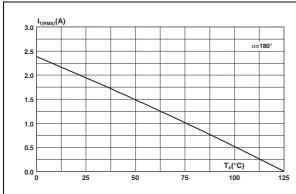


Figure 3. On-state rms current versus ambient temperature

Figure 4. Relative variation of thermal impedance versus pulse duration



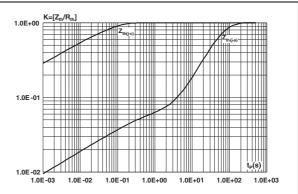
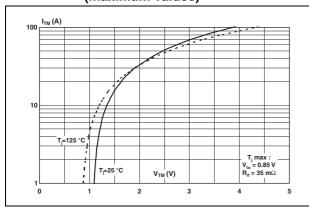
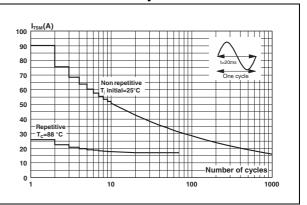


Figure 5. On state characteristics (maximum values)

Figure 6. Surge peak on state current versus number of cycles





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Figure 7. Non repetitive surge peak on state Figure 8. Relative variation of gate trigger current and gate trigger voltage current for a sinusoidal versus junction temperature

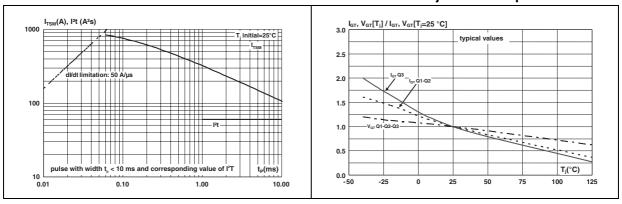


Figure 9. Relative variation of holding current and latching current versus junction temperature

Figure 10. Relative variation of critical rate of decrease of main current versus (dV/dt)c

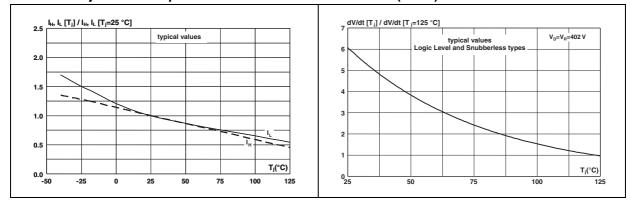
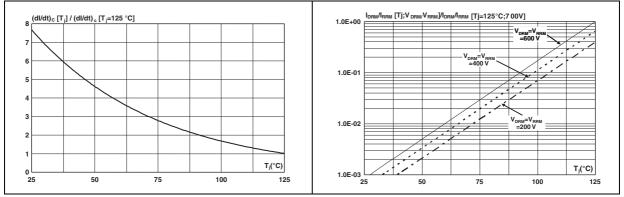


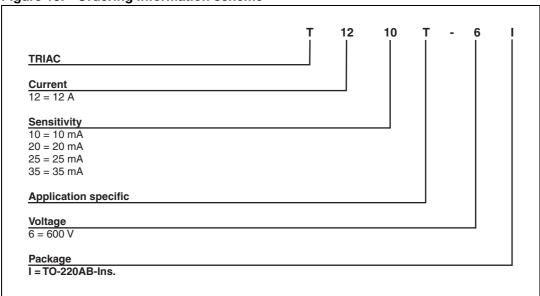
Figure 11. Relative variation of critical rate of Figure 12. Leakage current versus junction decrease of main current versus junction temperature

temperature for different values of blocking voltage (typical values)



2 Ordering information scheme

Figure 13. Ordering information scheme

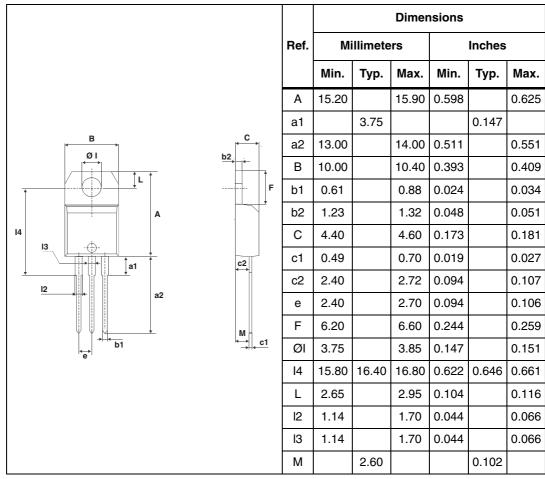


3 Package mechanical data

- Epoxy meets UL94, V0
- Recommended torque: 0.4 to 0.6 N⋅m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 6. TO-220AB insulated dimensions



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4 Ordering Information

 Table 7.
 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1210T-6I	T1210T-6I				
T1220T-6I	T1220T-6I	TO-220AB-ins.	2.3 g	50	Tube
T1225T-6I	T1225T-6I	10-220AD-IIIS.	2.5 g	50	Tube
T1235T-6I	T1235T-6I				

5 Revision history

Table 8. Document revision history

Date	Revision	Changes
23-Mar-2009	1	Initial release.

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